

Texas Instruments Technical Interview Questions and Answers

| Q1. Create an Ideal Op-Amp with a gain of 10. |
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| Q2. Enlist the relative and absolute timing parameters of an inverter? |
| Q3. Draw an edge triggered flip flop. |
| Q4. Design a divide by 3 counter |
| Q5. What is the time constant of an ideal integrator? |
| Q6. What are the characteristics of MOSFET? |
| Q7. How MOSFET act as a voltage controlled device? |
| Q8. What is RLC circuit? |
| Q9. Difference between broadcast domain Vs collision domain. |
| Q10. What is the relation between current and inductance? |
| Q11. What is setup time and hold time? |
| Q12. What is the purpose of a diode with relation to the motor in Y design? |





Q14. Difference between slotted aloha Vs time division multiplexing.

Q15. Name all the OSI layers.

Q16. What is subnetting?

Q17. Design 2 to 1 multiplexer using transmission gates.

Q18. What are the properties of Transistor?

Q19. What is Wein bridge oscillator?

Q20. What is darlington pair?